

## REMARKS

Claims 1, 2, 12 and 13 stand rejected under 35 U.S.C. § 102(e) as being anticipated by Applicant's Admission of Prior Art (AAPA). Claims 3, 4, 6, 7, 9-11, 14, 15, 17, and 19-21 stand rejected under 35 U.S.C. § 103(a) as being obvious over AAPA and United States Patent No. 6,407,960 (Egbert).

The invention, as set forth in independent claims 1 and 12, includes the general feature of an integrated processing unit, bootstrap memory holding a bootstrap program, and an internal system storage structure, where the processing unit executes at least a portion of the bootstrap program using an internal system storage structure for temporary storage without access to external memory.

The Office Action asserts that Applicants teach these features in the Background section of the present application. The Office Action cites the general memory mentioned in paragraph 0008 as providing the internal system storage structure for executing at least a portion of the bootstrap program without access to external memory. To the contrary, the term general memory is used to demonstrate that to execute the bootstrap program, the processor requires access to a memory resource in general. In Applicant's example this memory resource is the external memory, which cannot be accessed without *a priori* knowledge of its structure. Applicant does not state that this general memory is internal to the integrated device. Indeed the problem described in the Background section is that the processor executing the bootstrap program conventionally *requires* access to the external memory.

For these reasons, Applicants statements do not teach or suggest the use of an internal system storage structure by the processor to execute at least a portion of the bootstrap *without* access to external memory. Accordingly claims 1, 12, and all claims depending therefrom, are allowable. Applicants respectfully request the rejection of these claims be withdrawn.

Egbert fails to correct this deficiency. In fact, Egbert uses the external memory directly for configuring the its memory interfacce. "Hence, the device registers 12 may be selectively programmed with register data values stored in the external memory..." (Col. 3, ll. 26-28). Egbert does not teach or suggest using an internal system storage structure for use by the processor in executing a bootstrap program without access to external memory.

Egbert teaches away from the present invention by requiring access to external memory to complete the configuration.

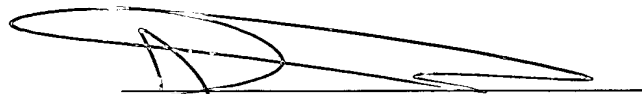
For these additional reasons, claims 1, 12, and all claims depending therefrom, are also allowable over the combination of AAPA and Egbert. Applicants respectfully request the rejection of these claims be withdrawn.

In view of the remarks set forth herein, the application is believed to be in condition for allowance and notice to that effect is solicited. Nonetheless, should any issues remain that might be subject to resolution through a telephonic interview, the examiner is requested to contact the undersigned attorney with any questions, comments or suggestions relating to the referenced patent application.

It is believed that no fee is due as a result of this response. However, the Commissioner is authorized to charge any fees under 37 CFR § 1.17 that may be due on this application to Deposit Account 17-0055. The Commissioner is also authorized to treat this amendment and any future reply in this matter requiring a petition for an extension of time as incorporating a petition for extension of time for the appropriate length of time as provided by 37 CFR § 136(a)(3).

Respectfully submitted,

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